

ES5A THRU ES5J 5.0 AMP SURFACE MOUNT SUPER FAST RECTIFIERS

FEATURES

- * Ideal for surface mount applications
- * Easy pick and place
- * Built-in strain relief
- * Low forward voltage drop

MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-0 rate flame retardant
- * Metallurgically bonded construction
- * Polarity: Color band denotes cathode end
- * Mounting position: Any
- * Weight: 0.093 grams

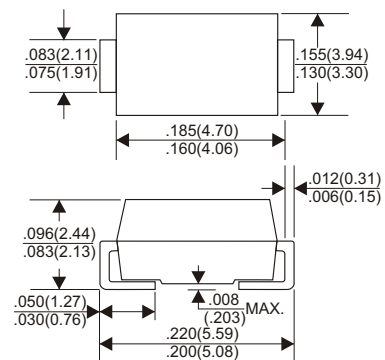
VOLTAGE RANGE

50 to 600 Volts

CURRENT

5.0 Ampere

DO-214AA(SMB)



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwise specified.
Single phase half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

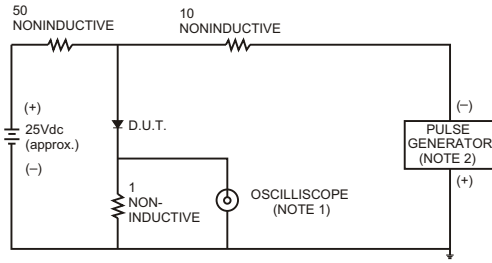
TYPE NUMBER	ES5A	ES5B	ES5C	ES5D	ES5E	ES5G	ES5J	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	150	200	300	400	600	V
Maximum RMS Voltage	35	70	105	140	210	280	600	V
Maximum DC Blocking Voltage	50	100	150	200	300	400	600	V
Maximum Average Forward Rectified Current at T _L =100°C	5.0							A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	120							A
Maximum Instantaneous Forward Voltage at 5.0A	0.95		1.25			1.75		V
Maximum DC Reverse Current at Rated DC Blocking Voltage	5			500				μA
Maximum Reverse Recovery Time (Note 1)	35							nS
Typical Junction Capacitance (Note 2)	40			25				pF
Operating and Storage Temperature Range T _J , T _{STG}	-65 — +150							°C

NOTES:

- Reverse Recovery Time test condition: I_F=0.5A, I_R=1.0A, I_{RR}=0.25A
- Measured at 1MHz and applied reverse voltage of 4.0V D.C.

RATING AND CHARACTERISTIC CURVES (ES5A THRU ES5J)

FIG.1- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm, 22pF.
2. Rise Time= 10ns max., Source Impedance= 50 ohms.

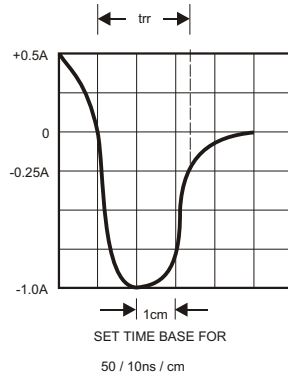


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

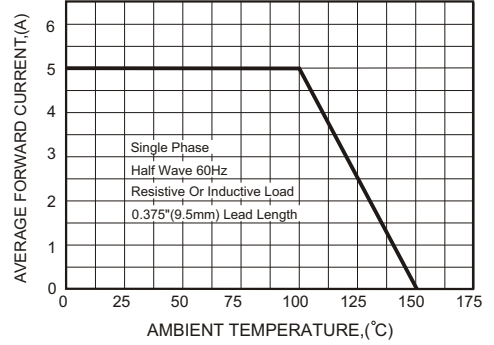


FIG.3-TYPICAL FORWARD CHARACTERISTICS

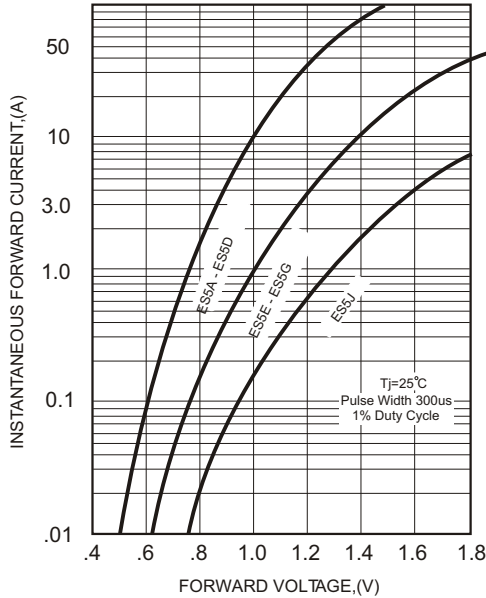


FIG.4-TYPICAL REVERSE CHARACTERISTICS

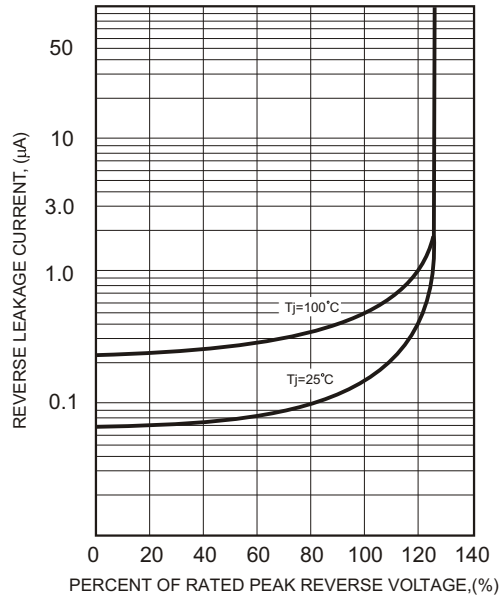


FIG.5-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

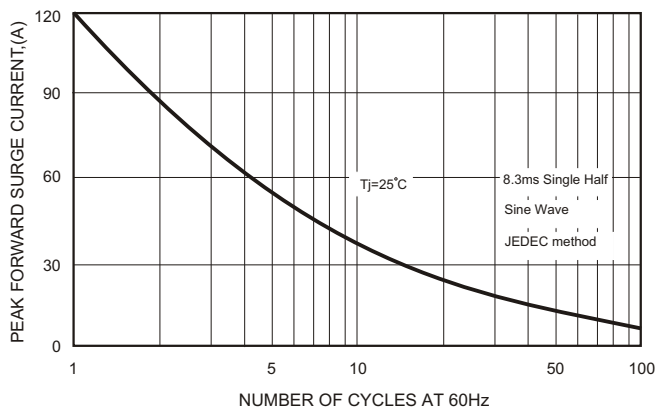


FIG.6-TYPICAL JUNCTION CAPACITANCE

